



Junior ASIC Verification Engineer

Contract type: full-time

Job location: employment will be in Belgrade - Serbia with possible travel abroad

Company description

NoBug is an international expert digital design verification company that masters a full range of technologies (functional, formal, assertion-based) with a variety of tools and languages (Specman, RuleBase, SystemVerilog, Verilog-PLI/C) and wants to expand its activities with Serbian experts.

NoBug teams distinguish themselves by the proven capacity to deliver turn-key, high quality ASIC and SOC validation.

Through flexibility and quick response to the changing industry, NoBug proved to be positively thinking, innovative and progressive supplier for its customers - leading edge companies such as Intel, Broadcom, Texas Instruments, Cisco Systems, AMCC.

NoBug has completed the registration process in Serbia and now operates as a locally registered company with the same name. NoBug Serbia has offices with location in the center of Belgrade.

NoBug is looking for ASIC verification engineers who are ready to contribute to the developing of the verification and design service center in Serbia for its worldwide customers.

Requirements

Graduate Computer Science/Electronics/Mathematics/Physics

Knowledge of Digital Integrated Circuits

Knowledge of Hardware Description Languages (VHDL/Verilog) desired

Knowledge of finite state machines, graph theory desired

Knowledge of C programming

Good English knowledge is a must

Candidate must have an open mind and ability to learn a new design and new verification methodologies.

Candidate must possess good communication skills and strong interpersonal skills.

Following skills will be a plus

- Familiar with System Verilog or Specman

- Familiar with scripting tools and languages (e.g. bash, csh, awk, Perl)

- Familiar with development tools (e.g. make and versioning tools (e.g. CVS, RCS))

- Familiar with methodologies such as (from higher to lower interest): uVM, eRM, VMM.

- Familiar with Unix/Linux

- Knowledge of OOP (C++, Java)

Responsibilities

Review RTL architectural and implementation specifications.

Develop or maintain ASIC verification environments to support ASIC development.

Maintain existing ASIC verification environments.

Create stimulus drivers, monitors, dataflow models, and test plans to verify function and performance of advanced multiprotocol networking Asics.

Define and develop application tests required to verify ASICs meet functional and performance goals.

Define and implement functional coverage plans.

Develop testing and regression methodologies for new verification flow.

Develop /maintain /enhance environment tools /scripts /make files.
Evaluate CAD tools used in the verification process and provided by top-most EDA companies; understand and report tool bugs, provide EDA company feedback about tool usage and suggestions for improvement.

Offer

Motivating salary.

Flexible working hours.

Internal trainings.

Building career in verification.

Working with market leads in the domain.

Opportunities long/short business trips.

NoBug provides full support from the Romanian centers (Bucharest, Brasov and Iasi).

NoBug offers challenging state of the art projects with some of the biggest IC manufacturers, permanent professional skills upgrade in in-house training center, pleasant working conditions and professional relation with all the employees.

Note

Please submit an English version of your CV to jobs_sr@nobufconsulting.ro