

Veriest

JUNIOR DESIGN & VERIFICATION ENGINEERS

Veriest is an international ASIC and FPGA design house, providing advanced design, verification solutions and IPs.

Applying our innovative methodologies and proven expertise, we provide our customers in the networking, communications, consumer electronics, automotive, neural networks and wireless data, with an unsurpassed competitive edge.

Veriest is based in Israel, operating development centers in Belgrade, Novi Sad and Nis.

We offer a career as a developer in the hardware industry and an opportunity to take part in the most interesting and challenging projects in the semiconductor area. Selected candidates will join our three months internship program in Verification (Specman, System Verilog, UVM, ERM) with fee.

QUALIFICATIONS INCLUDE:

- Final year student or fresh graduate with B.Sc. or M.Sc. degree in electrical engineering
- Knowledge of C programming language and OOP
- Basic knowledge of digital electronics
- Knowledge of Verification languages and methodologies (Specman, Verilog, System Verilog, eRM, UVM, VMM) - an advantage
- Creative and service provider abilities
- Fast learning curve
- Team player attitude
- English - communication and writing abilities

If you are looking for an environment where you can grow professionally, learn from the best in the field, balance work and life habits and have a pleasant stable environment, please submit your CV in English on **office-bg@veriests.com**.

For more details visit: www.veriests.com

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