PANTELIJA M. BRAJIĆ

Date of Birth: 6th April 1988
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EDUCATION

University of Belgrade, School of Electrical Engineering 2012 – present
Master degree studies – The Division of Electronics

University of Belgrade, School of Electrical Engineering 2012
Department: The Division of Electronics
Thesis: "CCII Based Oscillators"
Advisor: prof. dr Željko Aleksić
Grade point average: 7.65

Šabac Technical High School 2007
Department: Computer Aided Design of Mechanical Systems
Thesis: "Design of Single-Stage Cylindrical Gear Reducer"
Advisor: prof. Miloje Đurić
Grade point average: 4.46

Majur Dobrosav Radosavljević – Narod Primay School 2003

EXPERIENCE

Field Test, P3 Communications GmbH, Belgrade 2012

- I have done an internship in the company for a period of three months as part of team of Field Test department. I participated in several projects so I performed predefined tests on prototype mobile phones before they were released in the market. Tests are designed in such a way to check functionality and quality of phones, primarily interface to the network and firmware. Manufacturers of phones, which models I performed tests on are Nokia and Vertu. I took part in four projects which are done in different countries throughout Europe.

Embedded Systems, School of Electrical Engineering, Belgrade

- Structured programming TI MSP430 and ST M32F100 microcontrollers in assembly language and C
- Using embOS real-time operating system in applications for TI MSP430 and STM32F100 microcontrollers
- Testing designs on appropriate development boards

VLSI System Design, School of Electrical Engineering, Belgrade

- VHDL based design of simple processor using Xilinx ISE and its verification of the design on the Xilinx Spartan-3E development board
- Realization of FIR filter using FPGA Xilinx Spartan-3E

Digital Signal Processing, School of Electrical Engineering, Belgrade

- Low pass FIR filter design using MATLAB toolboxes and realization of the filter using TMS320C5416 digital signal processor

Microwave Electronics, School of Electrical Engineering, Belgrade
- Designing A-class amplifier with bipolar transistor in the 950- to 1025-MHz range and measuring scattering parameters of the amplifier using a network analyzer.

Integrated Circuit Design, School of Electrical Engineering, Belgrade
- Designing add-compare-select unit of Viterbi decoder using Magic, IRSIM and Pspice
- Designing low-voltage low-power CMOS operational amplifier using g_m/I_D methodology, designing amplifier layout using Electric VLSI Design System and simulation the layout using extracted parasitic parameters of the layout.

LANGUAGES

Serbian: native language

English: intermediate listener and speaker, intermediate reading and writing

Spanish: limited knowledge

COMPUTER SKILLS

Programming: C, C++, MATLAB, Pascal

Applications: MS Office, AutoCAD, Inventor, ProEngineer, SolidWorks, CorelDRAW, OrCAD, Altium Designer, NI LabWindows/CVI

Platforms: Linux, Windows

OTHER

Class C driving license