

Curriculum Vitae

Miloš Ergić

Basic information **Date of birth:** 27.05.1988.
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Education *Bachelor of Science, Electrical Engineering and Computer Science*

- School of Electrical Engineering, University of Belgrade – Department of Computer Engineering (2007 - 2014), currently attending Master degree studies (2014 -)
- Secondary school of Electrical Engineering „Nikola Tesla“ – Department of Computer Engineering (2003 – 2007)

Practical experience and projects

- **Diploma dissertation – Android application for consumer analysis (October 2014)**
Android application that allows creating and managing shopping lists and also predicts items for future purchasing based on previous shopping habits.
Languages: Java, Android SDK, SQLite; Tool: Eclipse.
- **Compiler for MicroJava developed for Compilers course (January 2014)**
The compiler is used to syntactically and semantically correct MJ programs which can be translated into bytecode and executed on MicroJava virtual machine.
Languages: Java, Jflex, CUP; Tool: Eclipse.
- **Kernel for multithreaded OS with time sharing developed for Operating systems 1 course (February 2012)**
Kernel has a subsystem for working with threads. It can create, start or destroy them and also uses semaphore concept and interrupt events.
Language: C++; Tool: bcc 3.1 compiler.
- **File system for OS developed for Operating System 2 course (June 2014)**
Implementation of a file system designed for partitions on standard hard drives. It uses bit vector for memory mapping and two level indexes for file allocation. File system has features like mounting, unmounting, formatting, opening (multiple readers – single writer) and deleting files.
Language: C++; Tool: Visual Studio 2010.

- **32-bit address pipeline processor based on ARM architecture with cache on FPGA chip developed for VLSI Supercomputers: Realization and Application course (June 2013)**
Processor has a five staged pipeline, jump prediction and 4 register banks for 3 different working modes. It can handle interrupts, recover from different kinds of hazards (data, control, structure) and communicate with the memory via cache. This project was done in a four people team.
Language: VHDL; Tools: Quartus, ModelSim.
 - **JobFair 12 – IT Coordinator**
A student project organized by EESTEC and BEST for graduates and students of technical faculties and also for companies that are offering employment. It included website management and improvement, database management and technical support for 2 days of the fair.
Languages: HTML, CSS, PHP, MySQL.
 - **Belgrade State of Mind vol2 – Logistics Coordinator**
International student conference for electrical engineering students from 10 different European countries.
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Computer skills

- Operating systems: Windows, Linux (basic knowledge)
 - Technologies: Java, Android SDK, C, C++, UML, VHDL, x86 assembly, MySQL, CSS/HTML, PHP.
 - Computer software: Eclipse, Microsoft Visual Studio, Altera Quartus, ModelSim.
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Personal skills

- Good communication skills
 - Cooperative, feels best when working in a team
 - Responsible
 - Ready for additional training and open for constructive criticism
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Also

- Excellent knowledge of English, written and spoken
- Driver's licence B category
- Member of student organization EESTEC
- Likes to play sports