Filip Tanasijevic

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Date of birth   08.12.1990

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Work experience

Internship in Soprex on QA possition   - 2 mounths

Education

Faculty of Electrical engineering,University of Belgrade

Department of Electronics   2009- 2014 (March)
Master student   2014- 2016 (expected)

High school in Ivanjica,
Deparament of natural science and mathematics 2005-2009

Foreign languages

English (active)

Skills

VHDL , Digital circuit , Digital design
C, Java, Hibernate framework
MatLab, Python, SQL, XML, HTML
Linux/Ubuntu advanced user
Computer networks (Cisco CCNA)ARP,RIP,OSPF,IGRP,NAT

Projects

Title: Security for safe
Description : VLSI systems, Syntesis on Xilinx Spartan 3E
Tools: VHDL

Title: ALU of CPU
Description : Integrated circuits, Layout design
Tools: Magic, Isim

Personal Charactr.	*Title*: PWM signals controled by AD conversion chanels *Metodology* : MSP430 microcontroler *Tools:* IAR embeded workbanch , C language
Hobby	Excellent skills in communication and organization Willingness and desire for continuous improvement Adaptability to work in a team to the realization of the objectives Loyalty, Commitment
Others	Basketball, Tennis, Gyming, Films, Music
	Driver license , B category (active driver)